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09/839596  
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	Class	Subclass

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

O.I.P.E.  
FA SCANNED Q.M.D. Q.A. CS

**PATENT DATE**

APPLICATION NO. 09/839596	CONT/PRIOR F	CLASS 351 25.1	SUBCLASS 3593	ART UNIT 2811	EXAMINER Wadav
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## APPlicants

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## RESURF LDMOS integrated structure

TITLE

PTO-2040  
12/89

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## **ISSUING CLASSIFICATION**

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
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